



PATENT  
0397-0439P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: H. MATSUMOTO, et al. Conf.: 5766  
Appl. No.: 10/054,823 Group: 2826  
Filed: January 25, 2002 Examiner: P. GREENE  
For: INTEGRATED SEMICONDUCTOR CIRCUIT DEVICE,  
PROCESS OF MANUFACTURING THE SAME, IC MODULE  
AND IC CARD

*#6/Election  
Answered  
9/17/02*

REPLY TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents  
Washington, DC 20231

September 12, 2002

Sir:

In reply to the Restriction Requirement dated August 22, 2002, the following remarks are respectfully submitted in connection with the above identified application.

REMARKS

Claims 1-10 are pending in the present application.

The Examiner has required election in the present application between:

Group I, claims 1-4 and 9-10, drawn to an integrated semiconductor circuit device, classified in class 257, subclass 471; and

Group II, claims 5-8, drawn to a process of manufacturing an integrated semiconductor circuit device, classified in class 438, subclass 15+.

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